

GA-F2A88XM-HD3

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09	HUDSON UMI, PCIE, PCI, CPU, LPC
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25	VCC_SB , APU_VDDP , VCC11_DUAL , VDDA25

APU_VDDIO_SUS=DDR15V

APU_VTT_SUS=DDRVTT

APU_VDDP_RUN=APU_VDDR_RUN=APU_VDDP

+1.1V_RUN=FCH_VDD_11_RUN=VCC_SB

+3.3V_RUN=VCC3

+3.3V_ALW=3VDUAL

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Title

COVER SHEET

Size

Custm

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GA-F2A85XM-HD3

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Component value change history

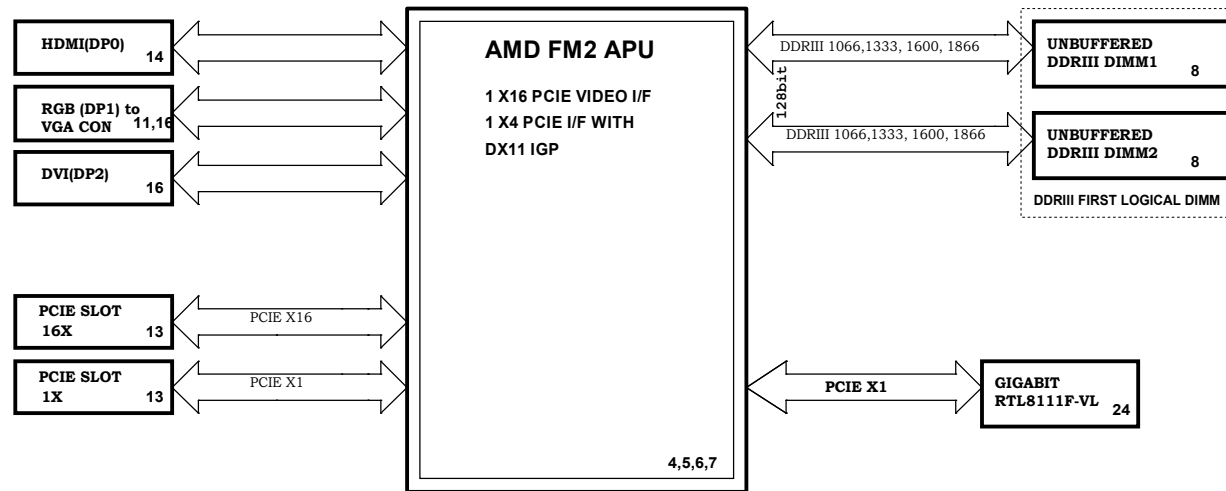
P-Code: U12065-0
243.84*174/B/50+/-15%/BLUE

[illegible]

Circuit or PCB layout change for next version

[illegible]

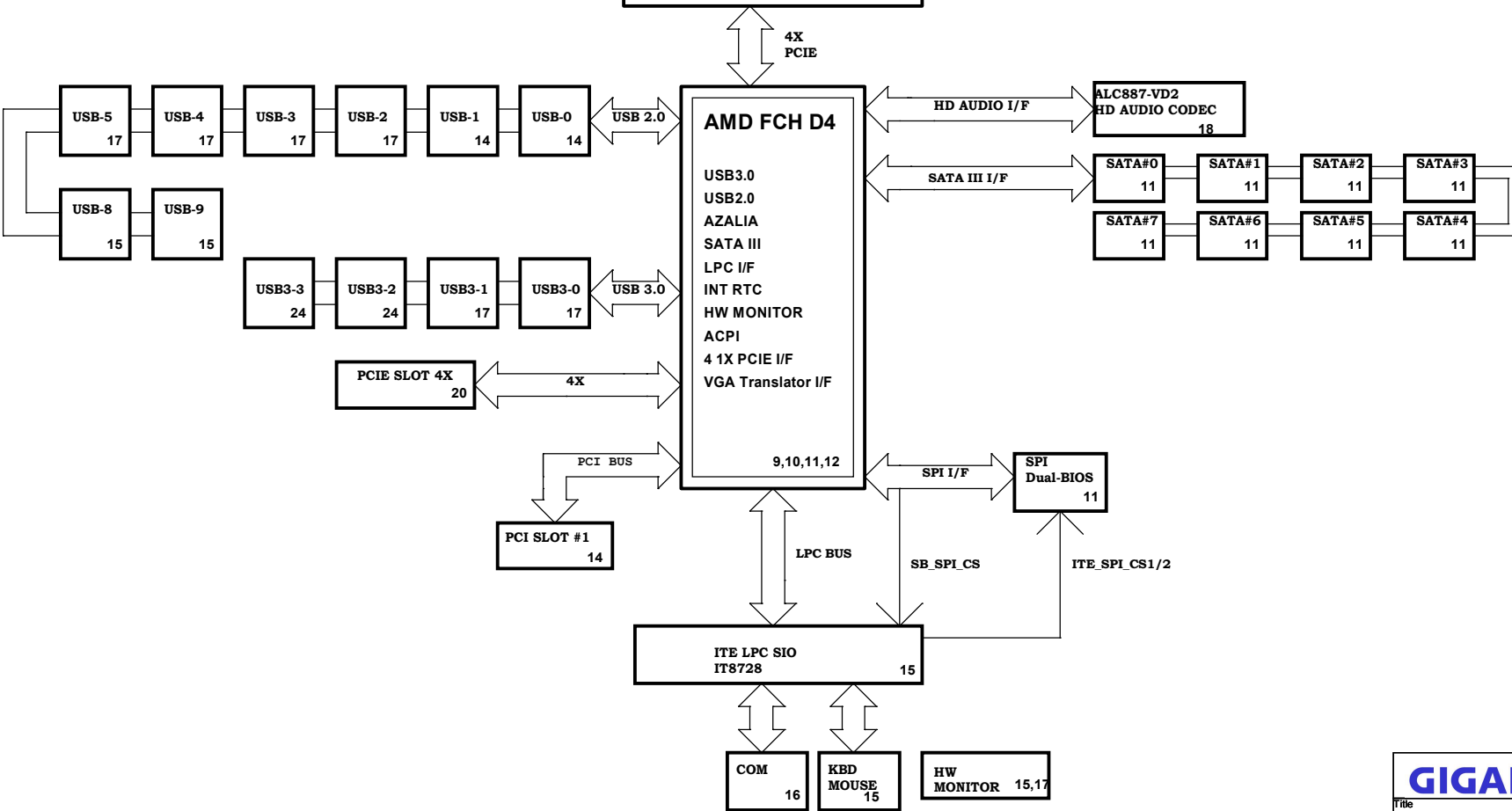
				
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BOM & PCB HISTORY				
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Fusion D3 CORE & PCIE POWER 25

DDR3 MEMORY POWER RT8120 23

APU POWER ISL62773 + ISL6608 21,22

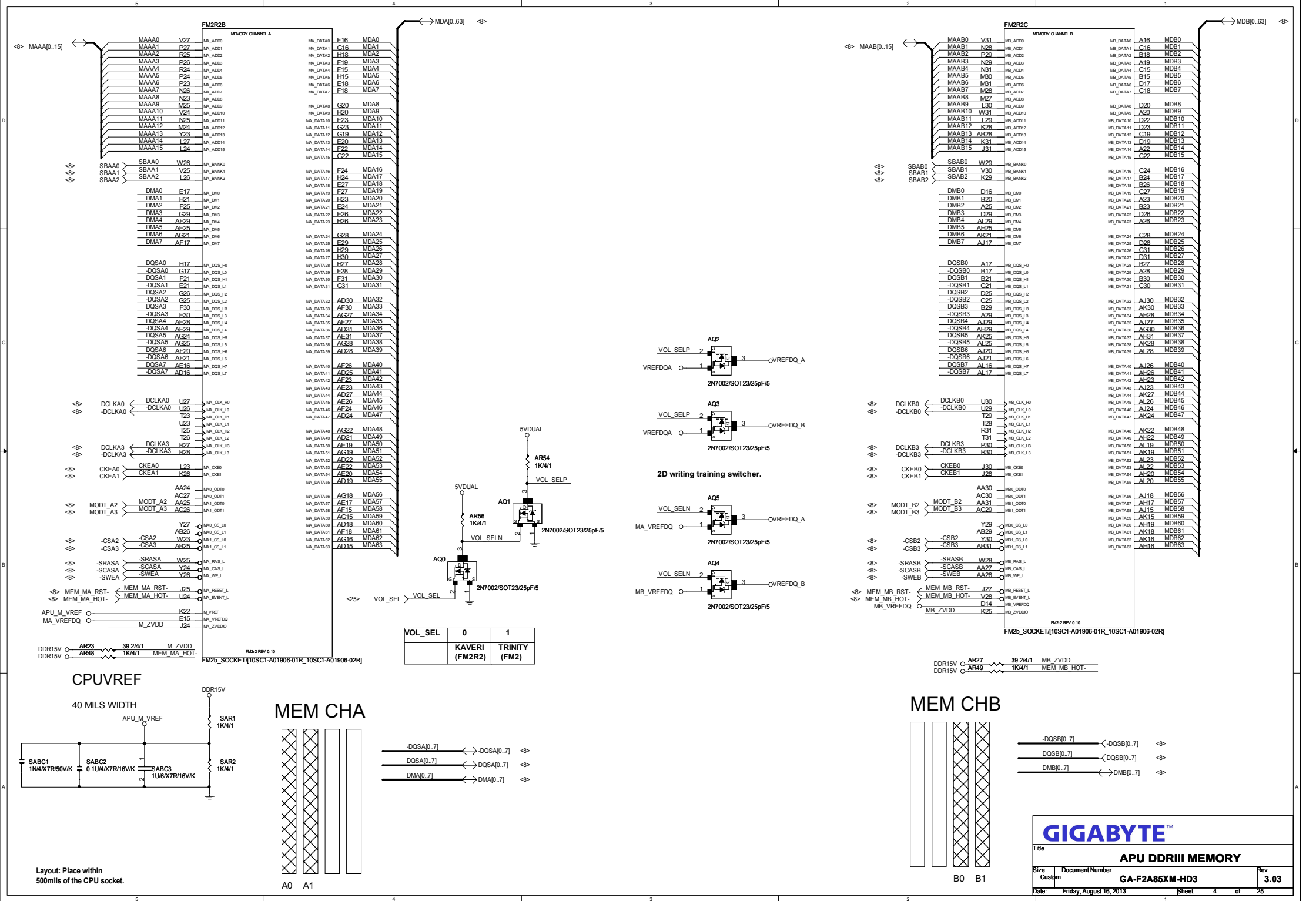


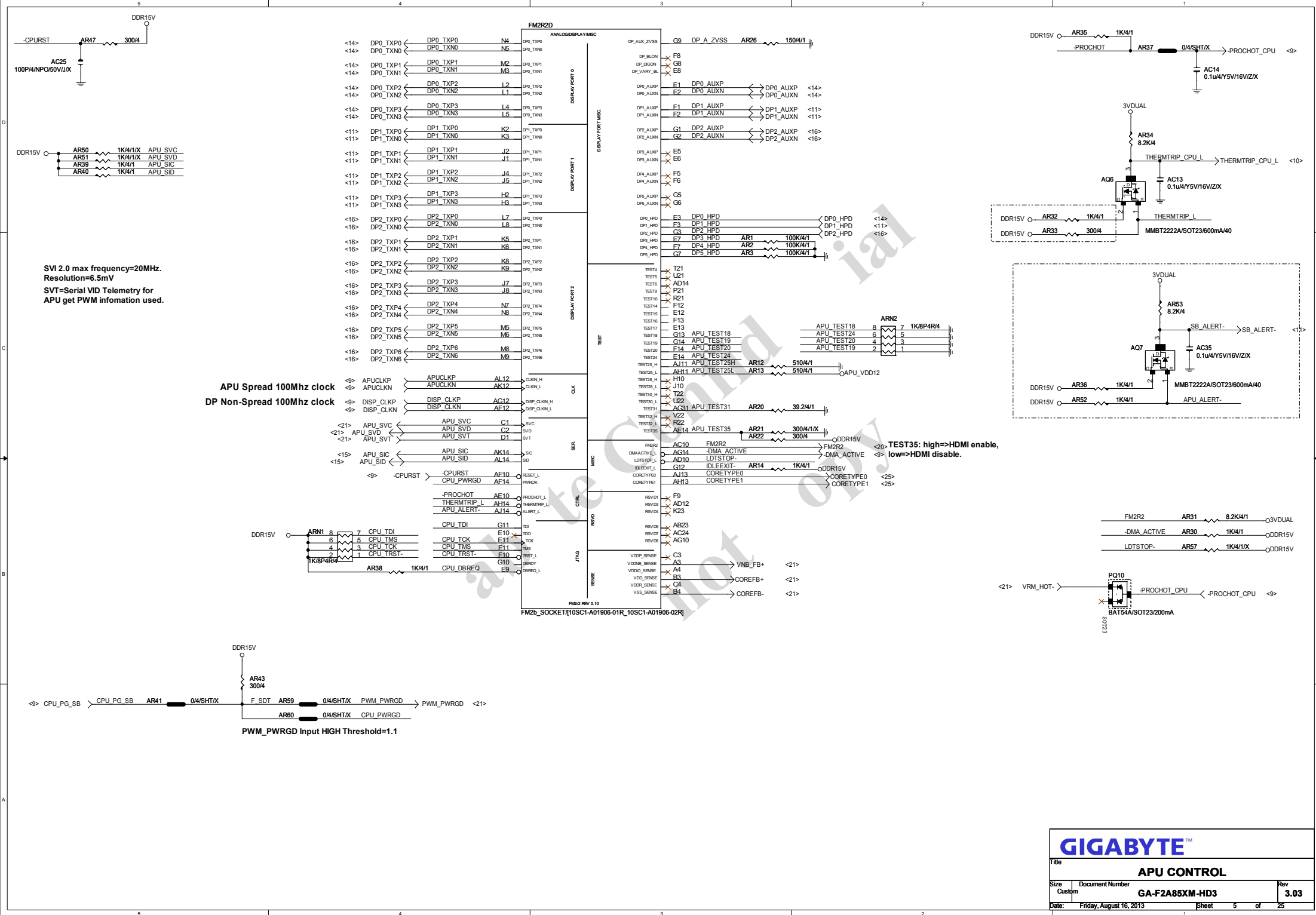
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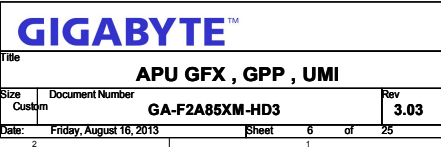
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Size: Custom Document Number: **GA-F2A85XM-HD3** Rev: **3.03**

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Short Protect
Check Short pad or oohm?

VCC3 - MR19 04 OVDDSPD

DDR15V O

MBC34 0.1u4X0R16/K

VDDSPD

MBC32 0.1u4X0R16/K

MBC33 0.1u4X0R16/K

VREFCA A

VREFDQ A

VREFDQ B

VREFDQ C

VREFDQ D

VREFDQ E

VREFDQ F

VREFDQ G

VREFDQ H

VREFDQ I

VREFDQ J

VREFDQ K

VREFDQ L

VREFDQ M

VREFDQ N

VREFDQ O

VREFDQ P

VREFDQ Q

VREFDQ R

VREFDQ S

VREFDQ T

VREFDQ U

VREFDQ V

VREFDQ W

VREFDQ X

VREFDQ Y

VREFDQ Z

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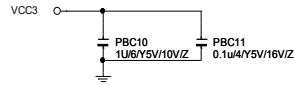
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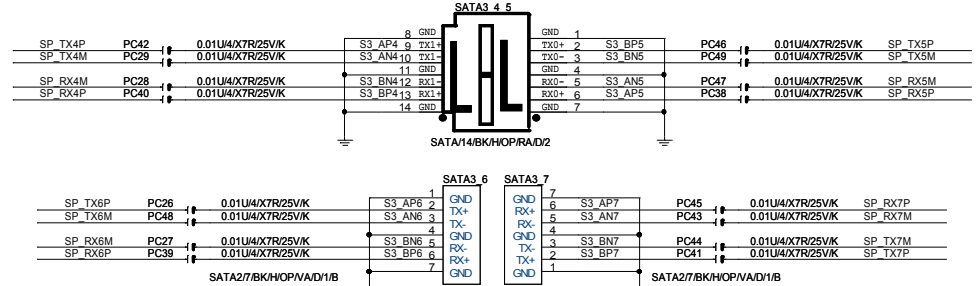
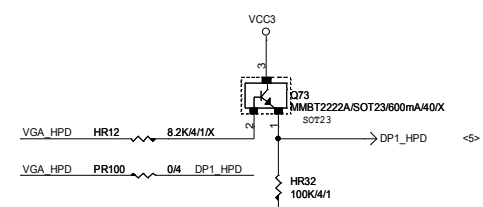
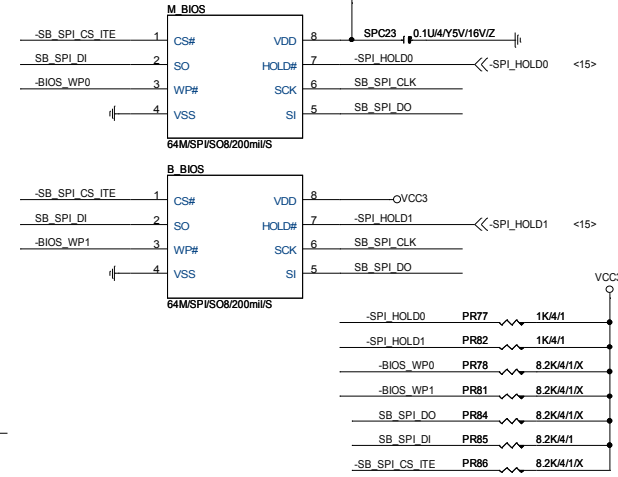
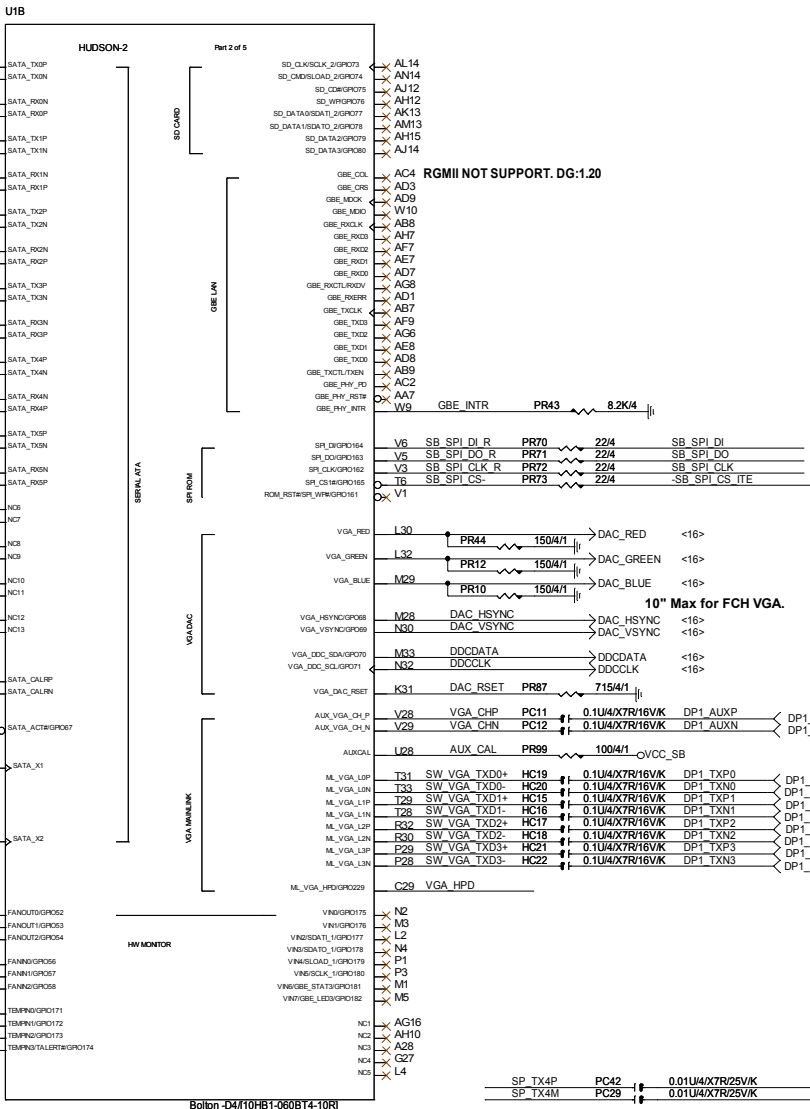
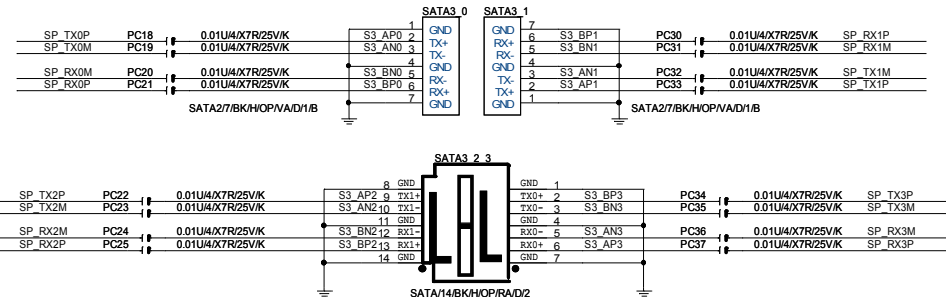
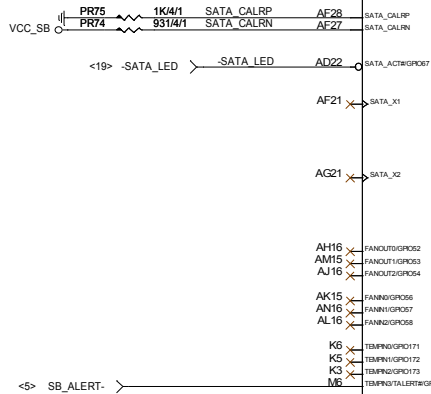
DDR30

1



SATA 6-7 for Hudson D4.

PLACE SATA_CAL RES VERY CLOSE TO BALL OF U1



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Title

Hudson D4 SATA/HWM/SPI

Size

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GA-F2A85XM-HD3

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3.03

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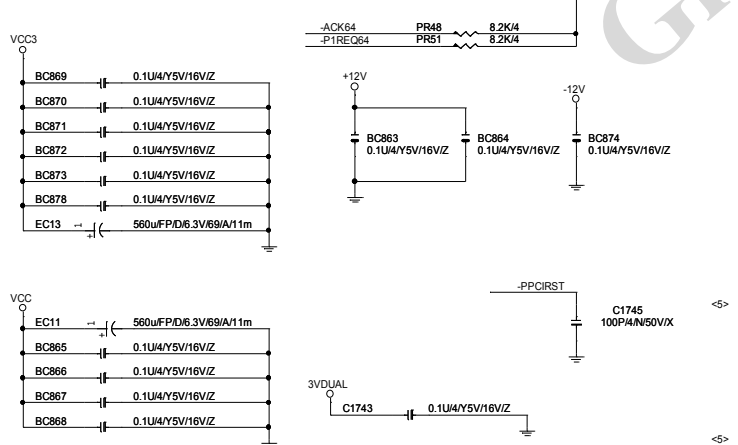
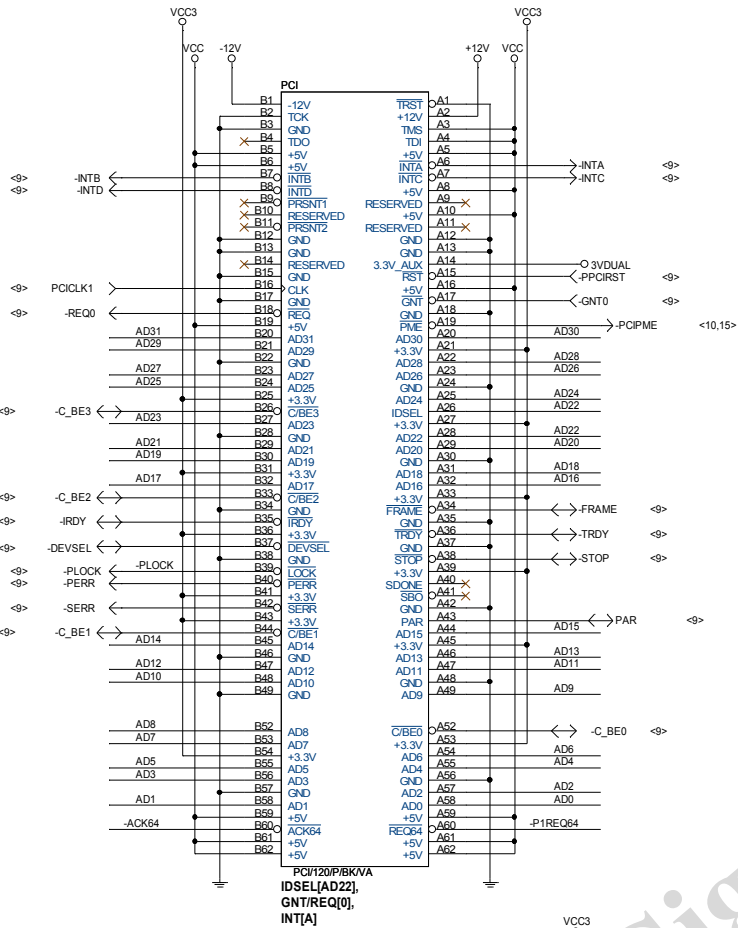
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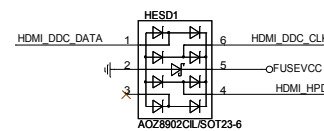
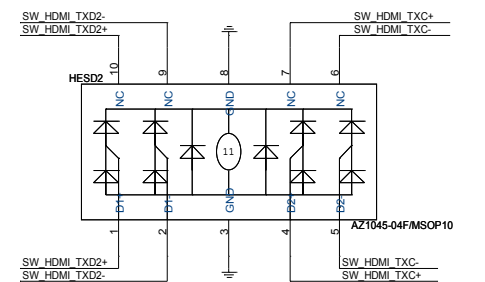
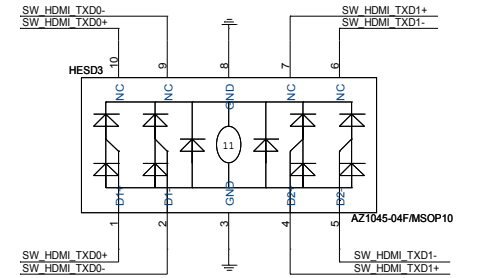
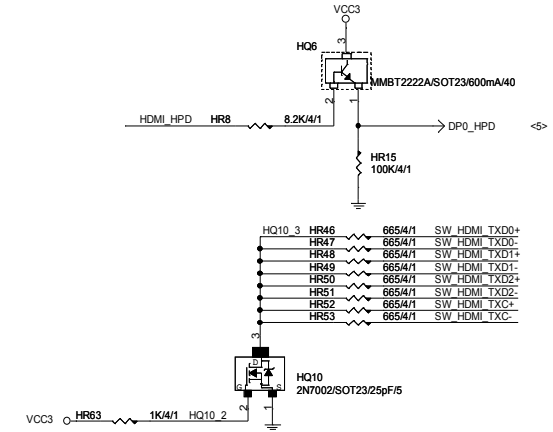
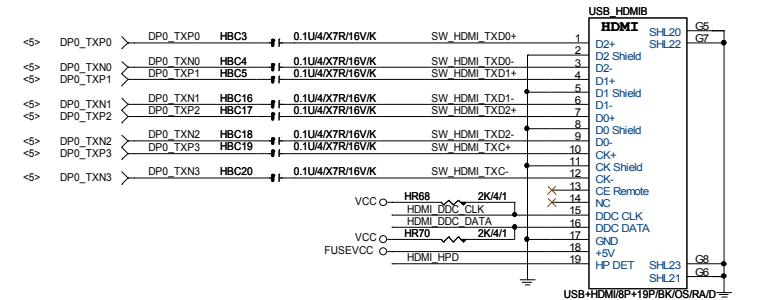
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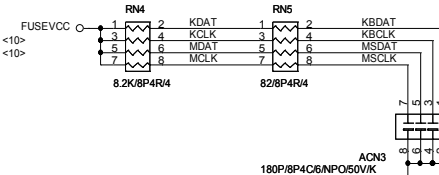
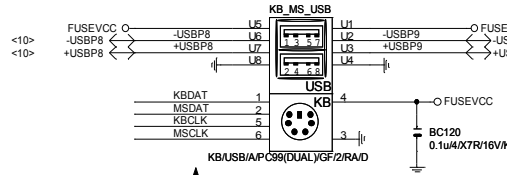
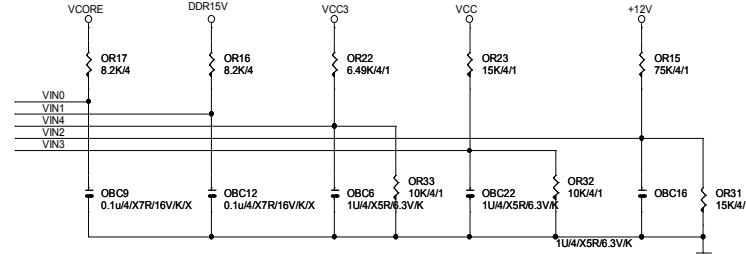
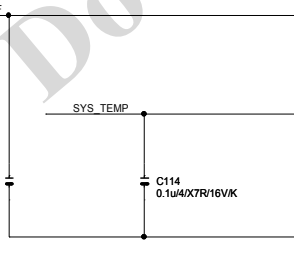
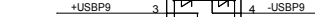
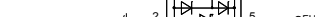
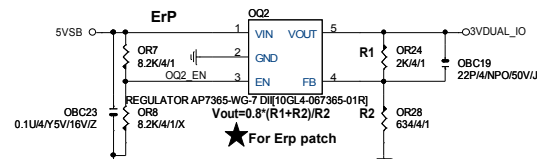
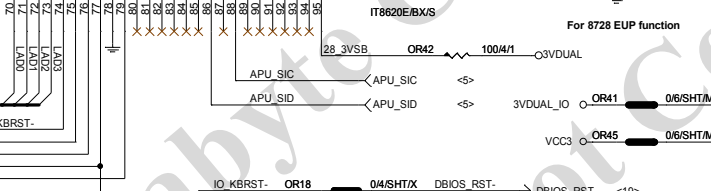
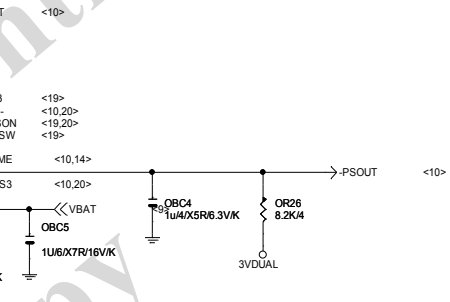
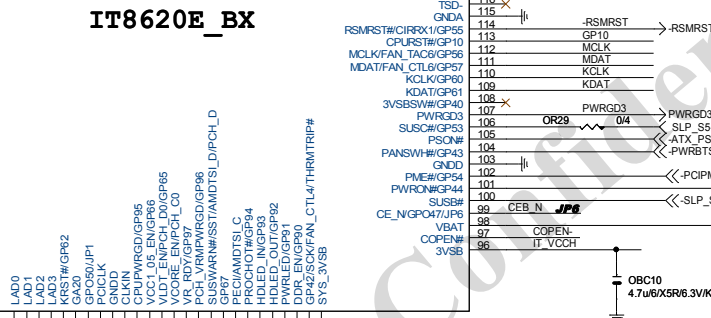
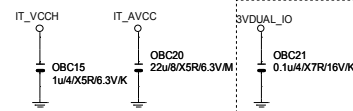
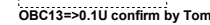
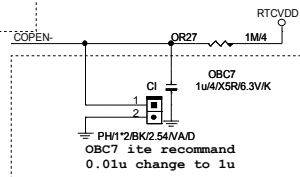
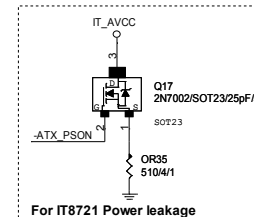
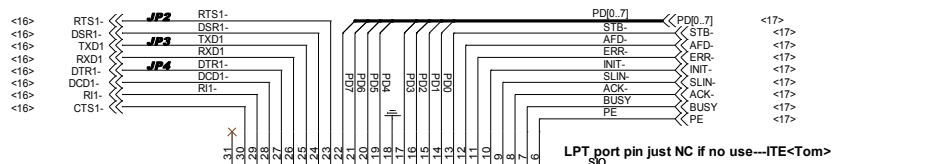
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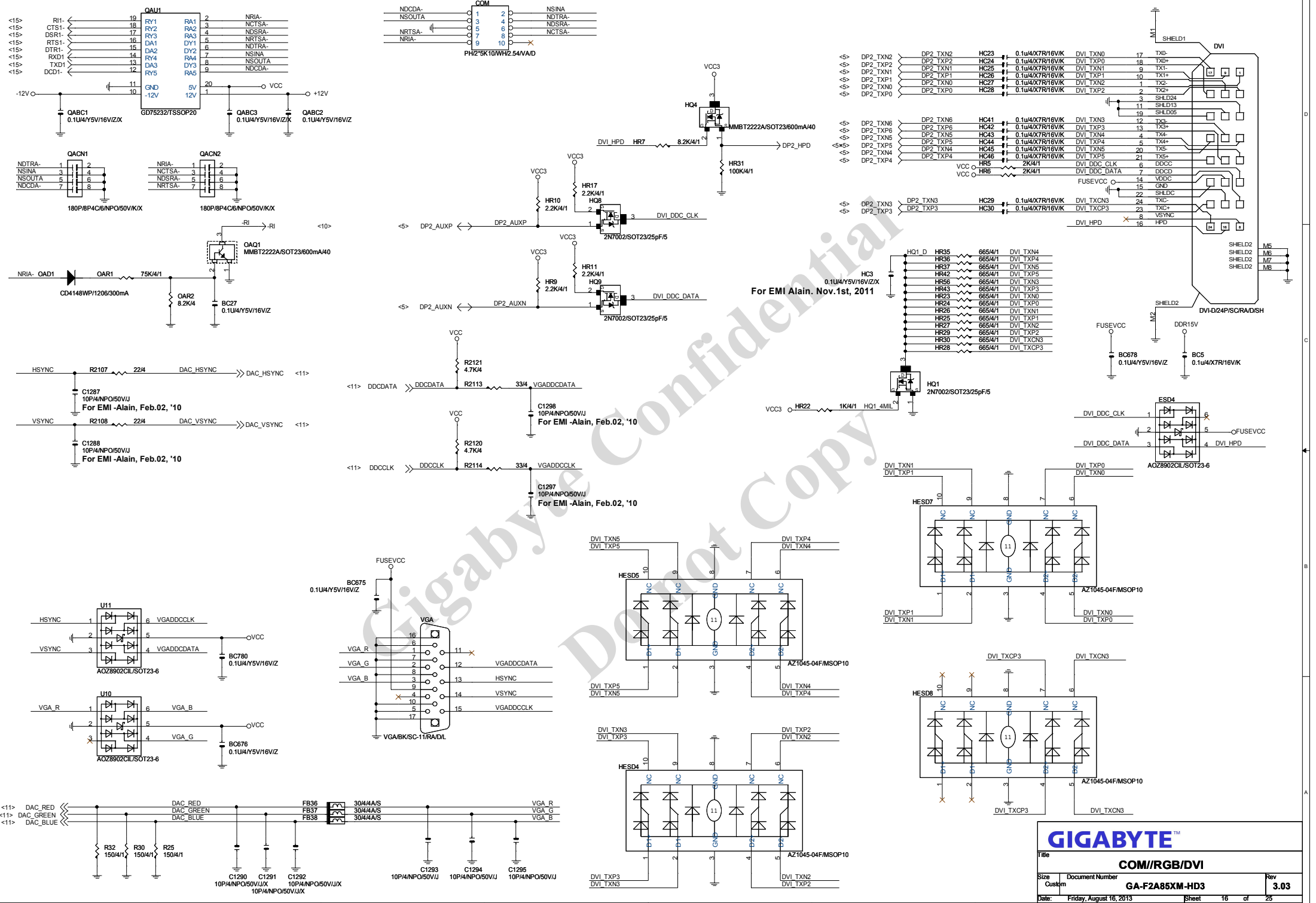


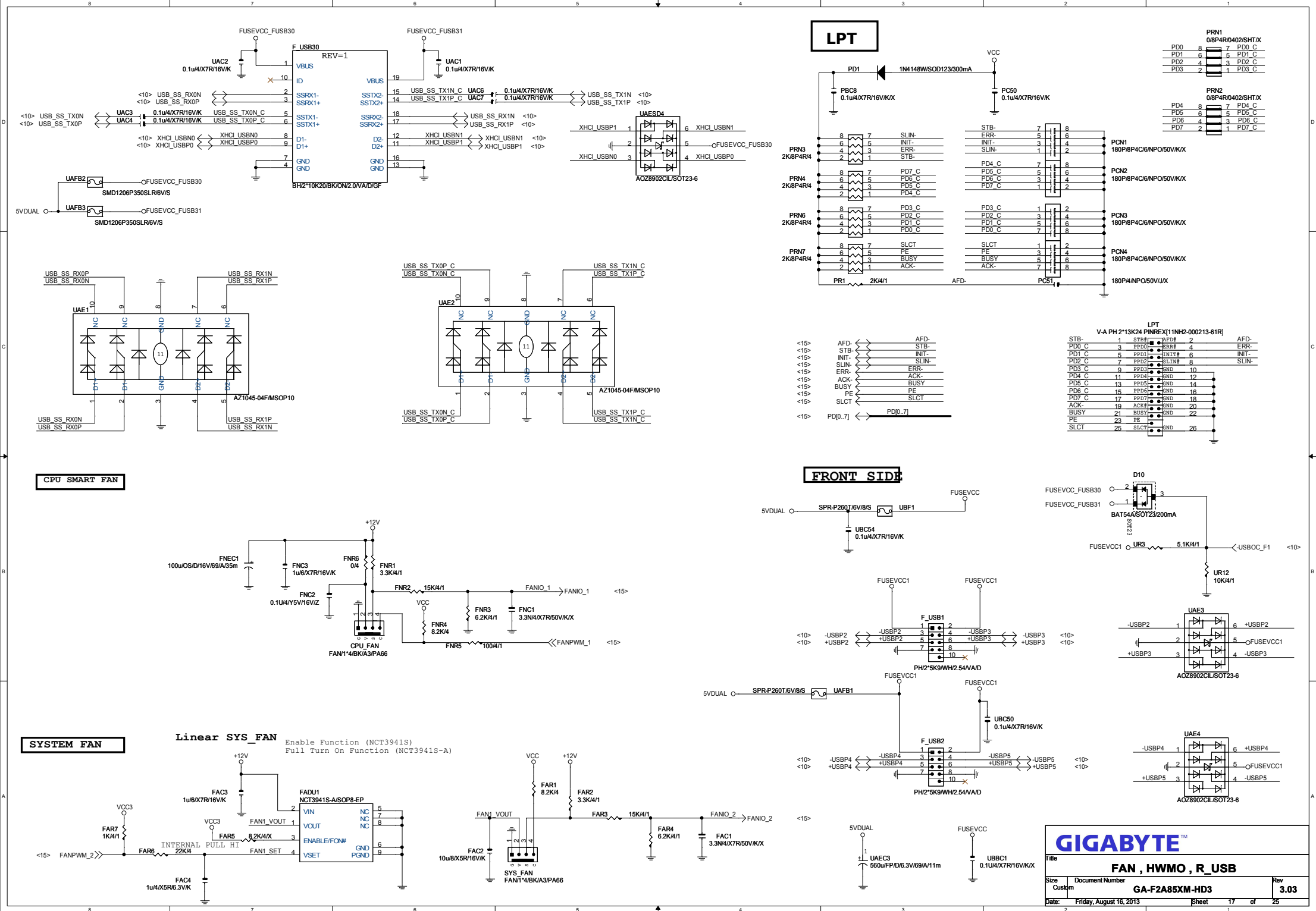
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GIGABYTE™		
Title: PCI SLOT ,HDMI		
Size: Custom	Document Number: GA-F2A85XM-HD3	Rev: 3.03
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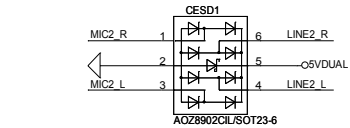
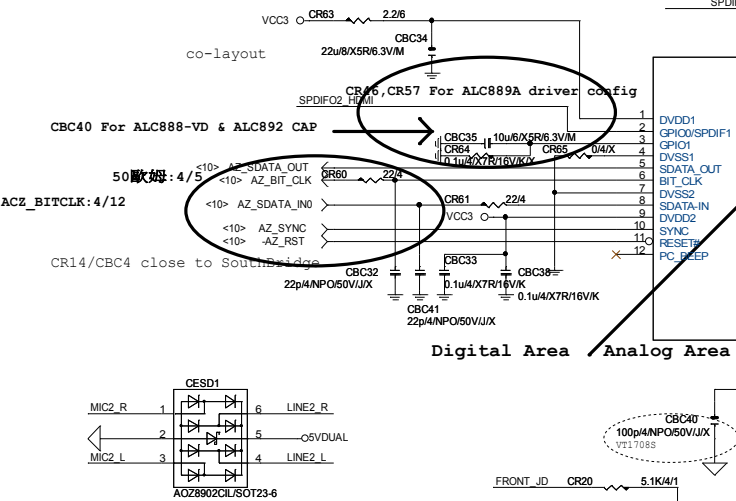
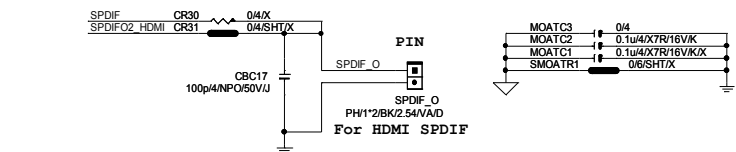
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SYSTEM FAN

FRONT SIDE

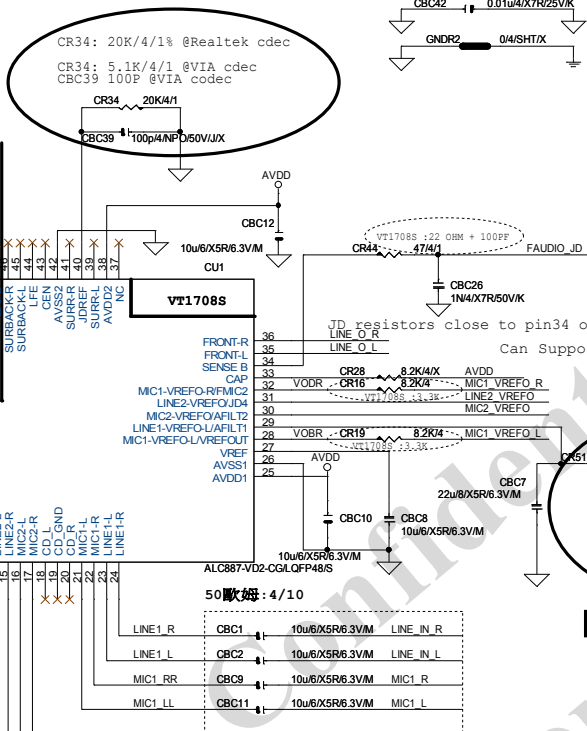
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SPDIF_OUT

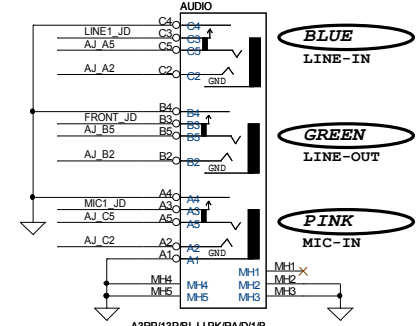
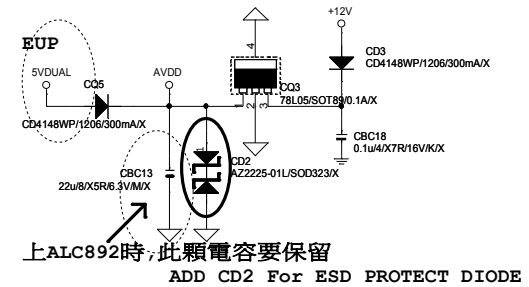


AZALIA CODEC ALC887-VD2/ALC889/VT1708S/VT1708SCE Colay

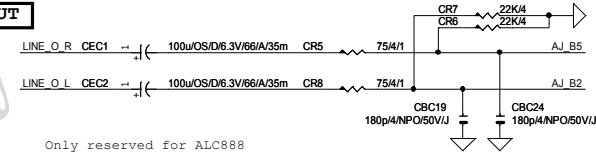
	ALC887-VD2	ALC889	VT1708S	VT1708SCE
CR65	X	O	O	X
CR64	X	X	X	O
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P	22ohm+100P
CR34	20K/1%	20K/1%	5.1K/1%	20K/1%
CR31	O	O	O	O
CR30	X	X	X	X
CBC1/CBC2	22uF/X5R	22uF/X5R	22uF/X5R	22uF/X5R
CR20	5.11K/4/1	5.11K/4/1	5.1K/4/1	5.1K/4/1
CBC35	O	X	X	O
CBC39/CBC40	N/A	N/A	100P/4	100P/4
CR6/CR7/CR54/CR58	22K/4	22K/4	10K/4	10K/4
R5/CR8/CR13/CR11/ R57/CR53	75 ohm	62 ohm	75 ohm	75 ohm
CR51/CD1/CBC7	O	X	X	O
CD2/CD3/CQ3/CQ5	X	O	O	X
R1/CR14/CR17/CR22	75 ohm	62 ohm	1K ohm	1K ohm



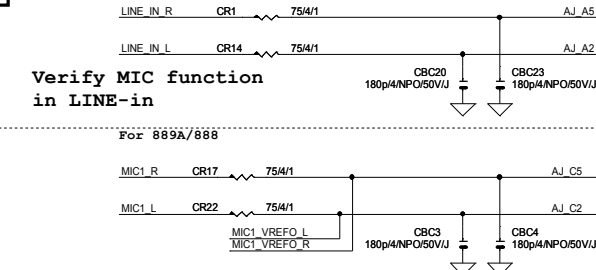
CODEC POWER/EMI PAD



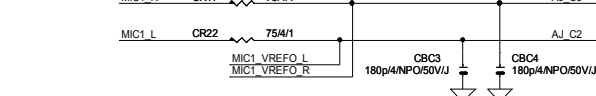
LINE-OUT



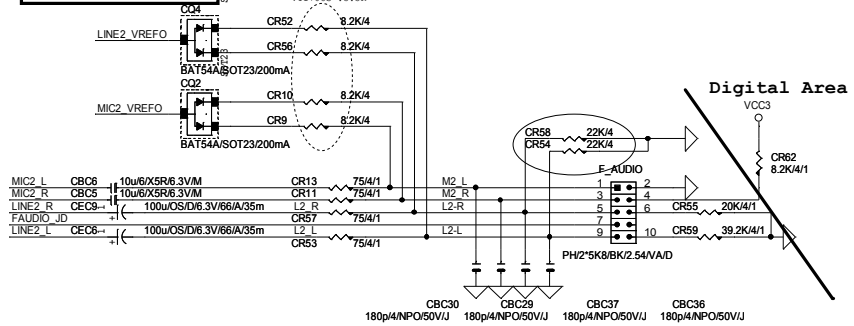
LINE-IN



MIC-IN

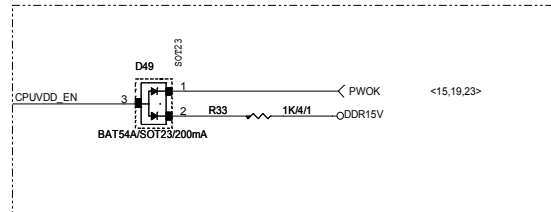


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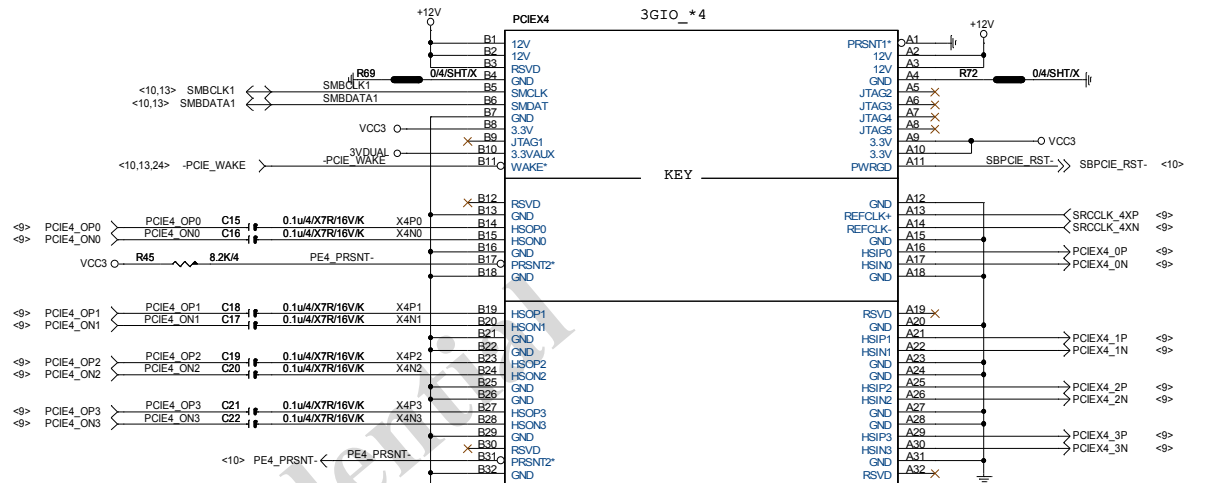
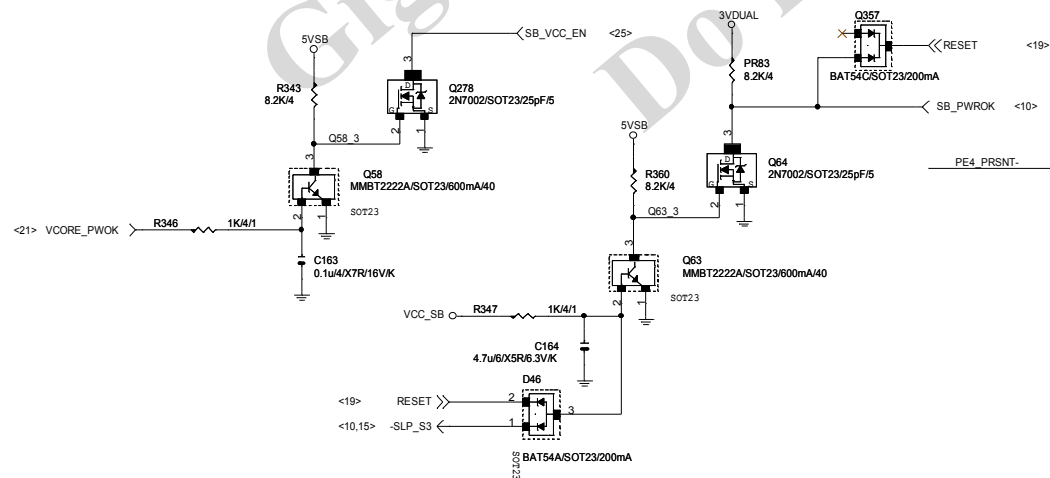
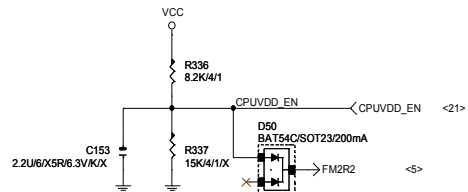


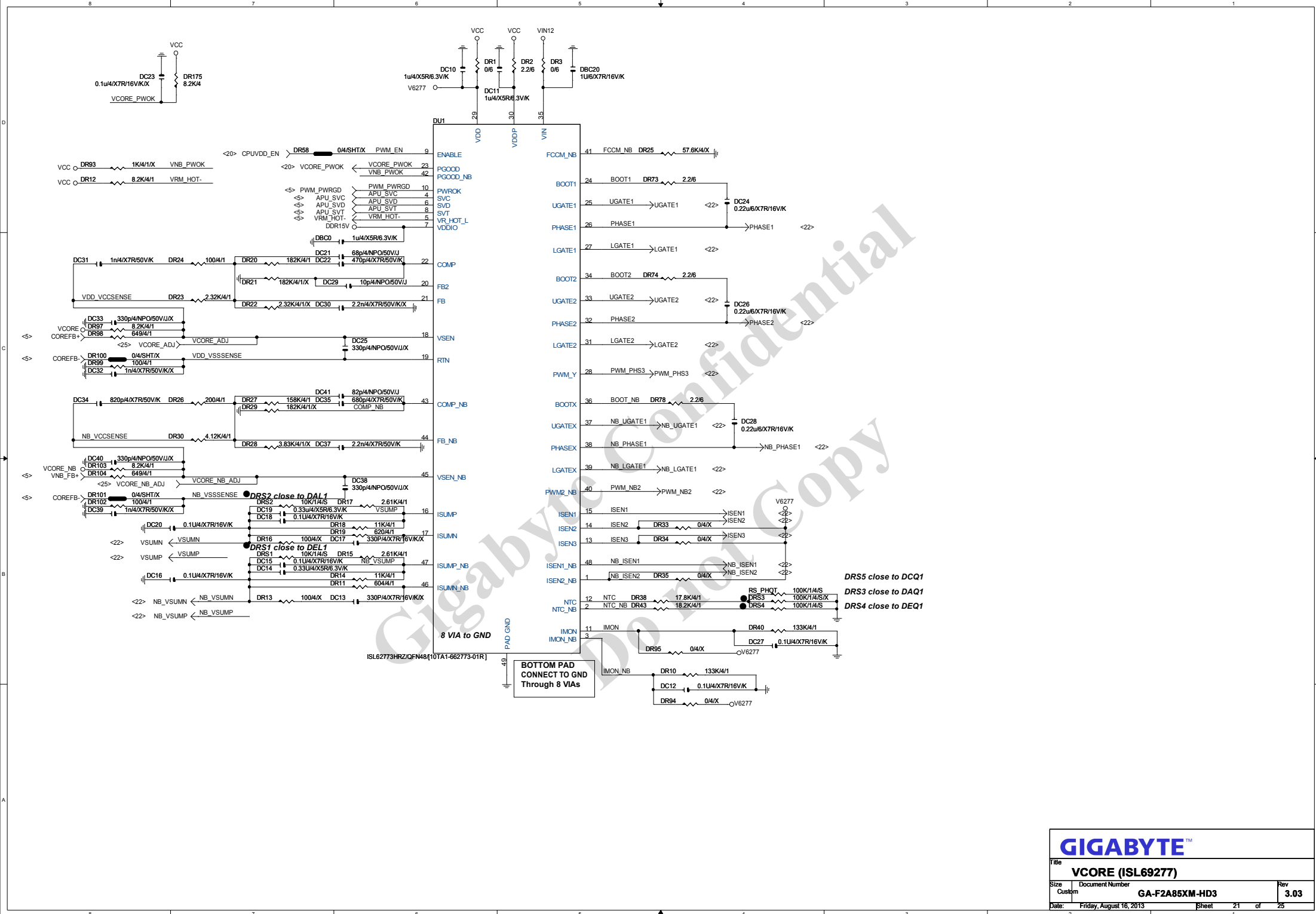
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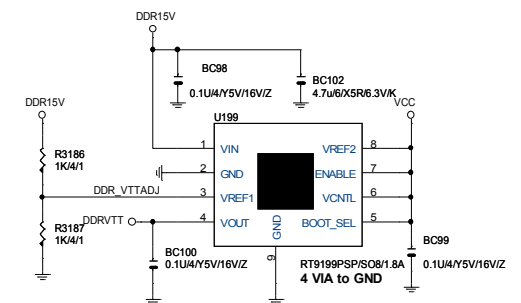
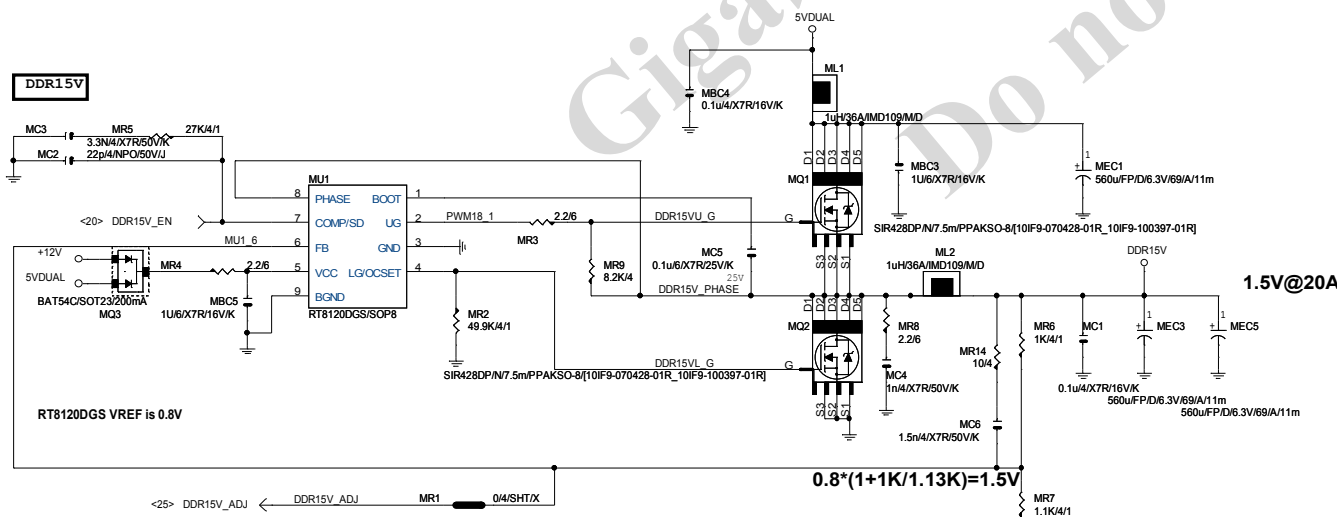
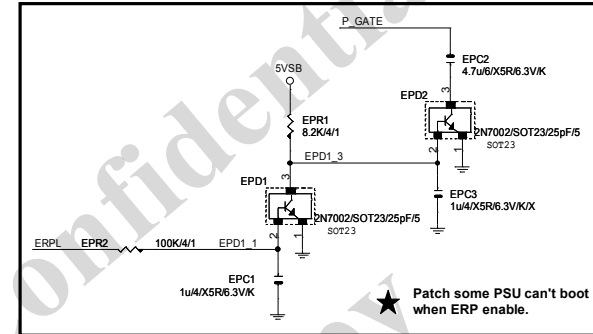
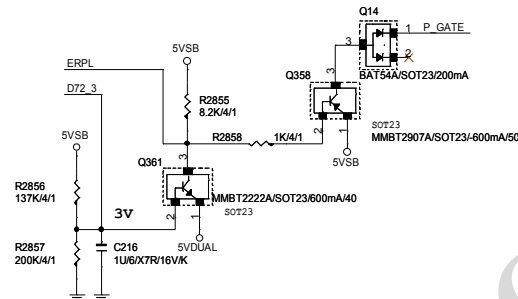
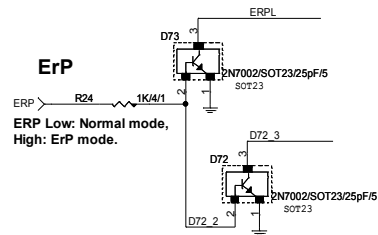
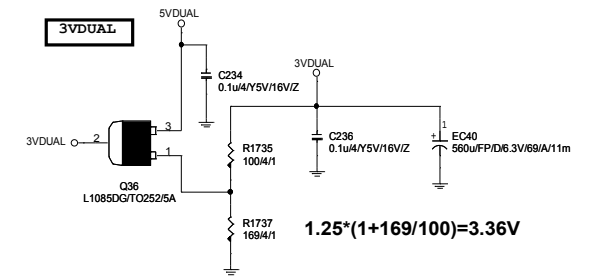
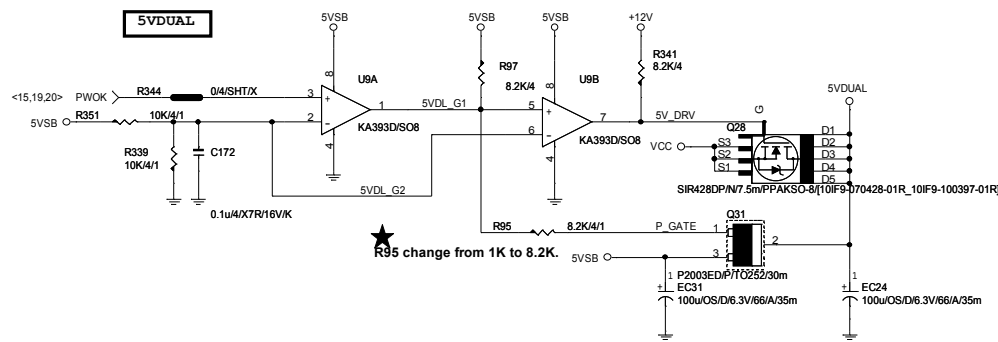
Rev : 1.0 pop

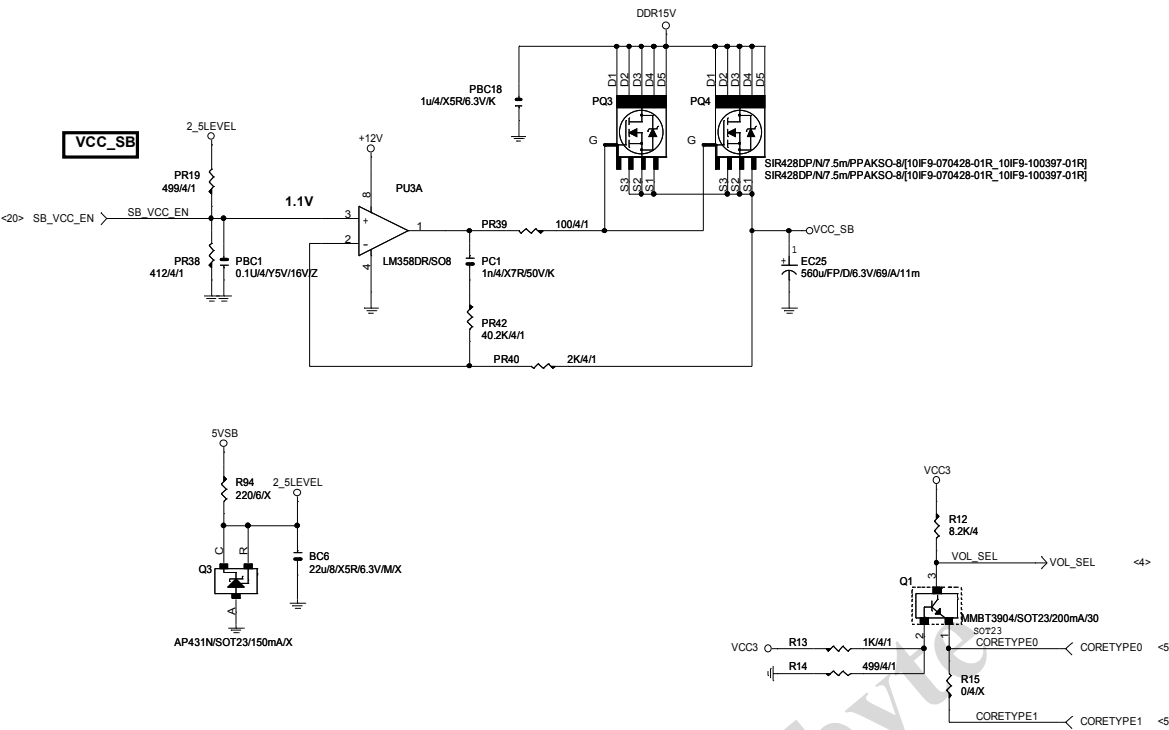


Rev: 0.1 pop



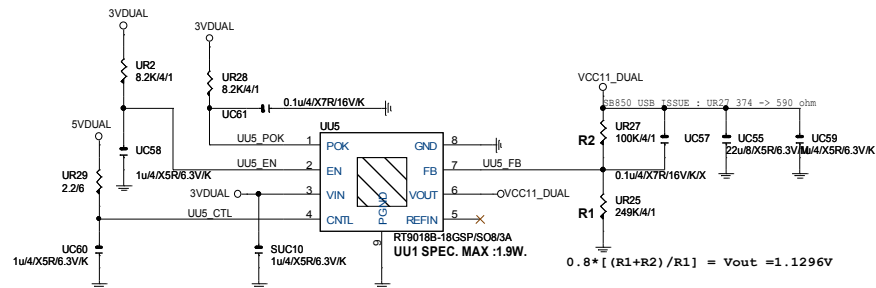




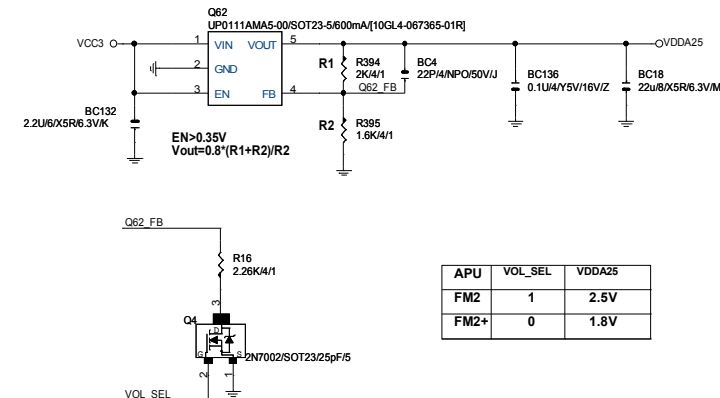
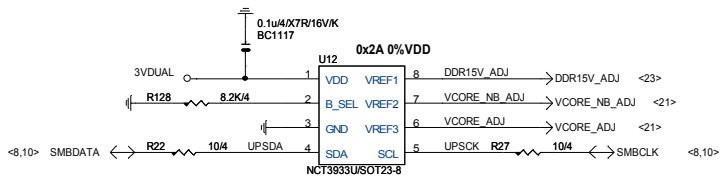


$V_{out} = 0.8 \cdot (R1 + R2) / R2$

APU	VOL_SEL	APU_VDD12
FM2	1	1.2V
FM2+	0	1.05V



【技術通報R&D技術通報156】
RT9018 (RICHTER) 與 NCT3730 (NUVOTON),
EM5103GE (EMC) 做共用, 針對 PIN7 (FB) 分壓阻值部份
(R1/R2) 須做修改為 100K 以上電阻值



APU	VOL_SEL	VDDA25
FM2	1	2.5V
FM2+	0	1.8V

GIGABYTETM

Title

VCC_SB, APU VDDP, VCC11_DUAL, VDDA25

Size

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